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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
(213.003-US)

In re Application of: YE ET AL.

Serial No: 10/677,563

Filed: OCTOBER 2, 2003

Title: METHOD AND APPARATUS FOR MONITORING  
INTEGRATED CIRCUIT FABRICATION

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) Group Art Unit: 2857  
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)  
) Examiner:  
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate of Mailing under 37 CFR 1.8**

I hereby certify that the attached Statement of Related Applications (1 page) is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

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on May 17, 2004.

  
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Signature

  
\_\_\_\_\_  
Print Name of Person Signing Certificate



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Title: **METHOD AND APPARATUS FOR MONITORING  
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Commissioner for Patents  
P.O. Box 1450  
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**STATEMENT OF RELATED APPLICATIONS**

Dear Sir:

The above-referenced application is related to the following applications:

1. "Method and Apparatus for Monitoring Integrated Circuit Fabrication",  
Serial No. 10/756,718, filed January 13, 2004 (still pending);
2. "Method and Apparatus for Monitoring Integrated Circuit Fabrication",  
Serial No. 10/763,142, filed January 22, 2004 (still pending); and
3. "Method and Apparatus for Monitoring Integrated Circuit Fabrication",  
Serial No. 10/763,433, filed January 23, 2004 (still pending).

Respectfully submitted,

Date: May 17, 2004

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